

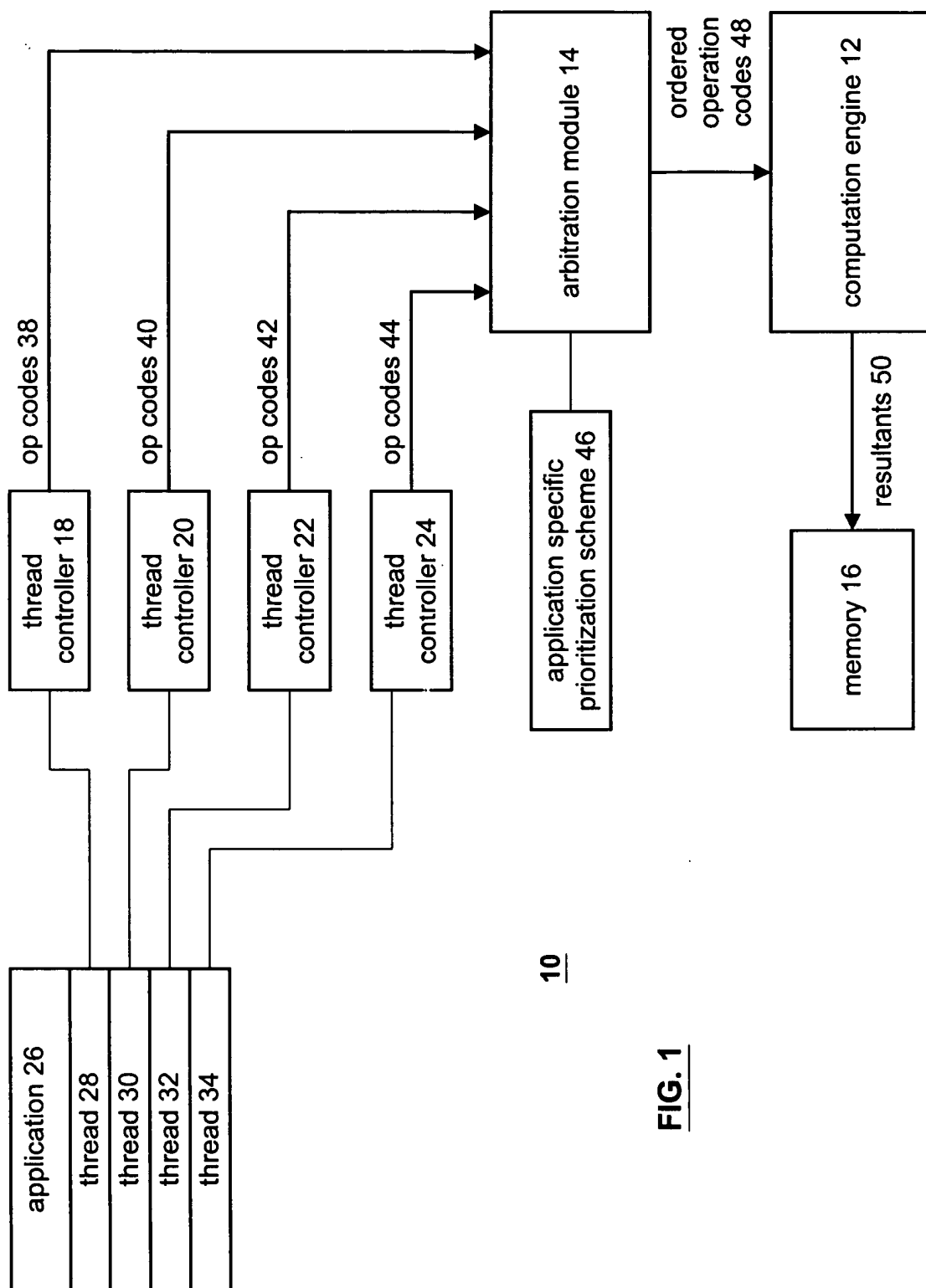
[illegible]

FIG. 1

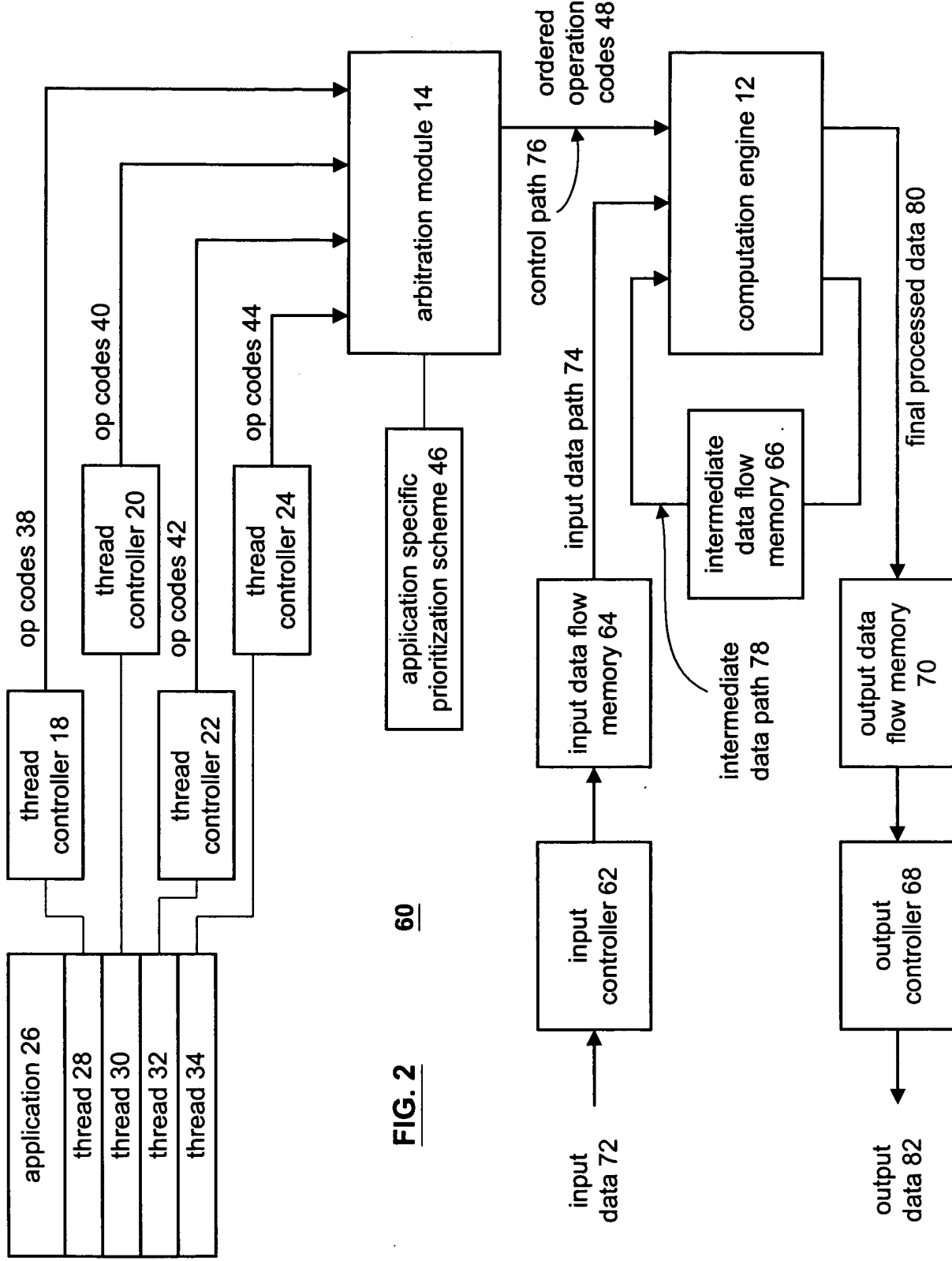


FIG. 2 60

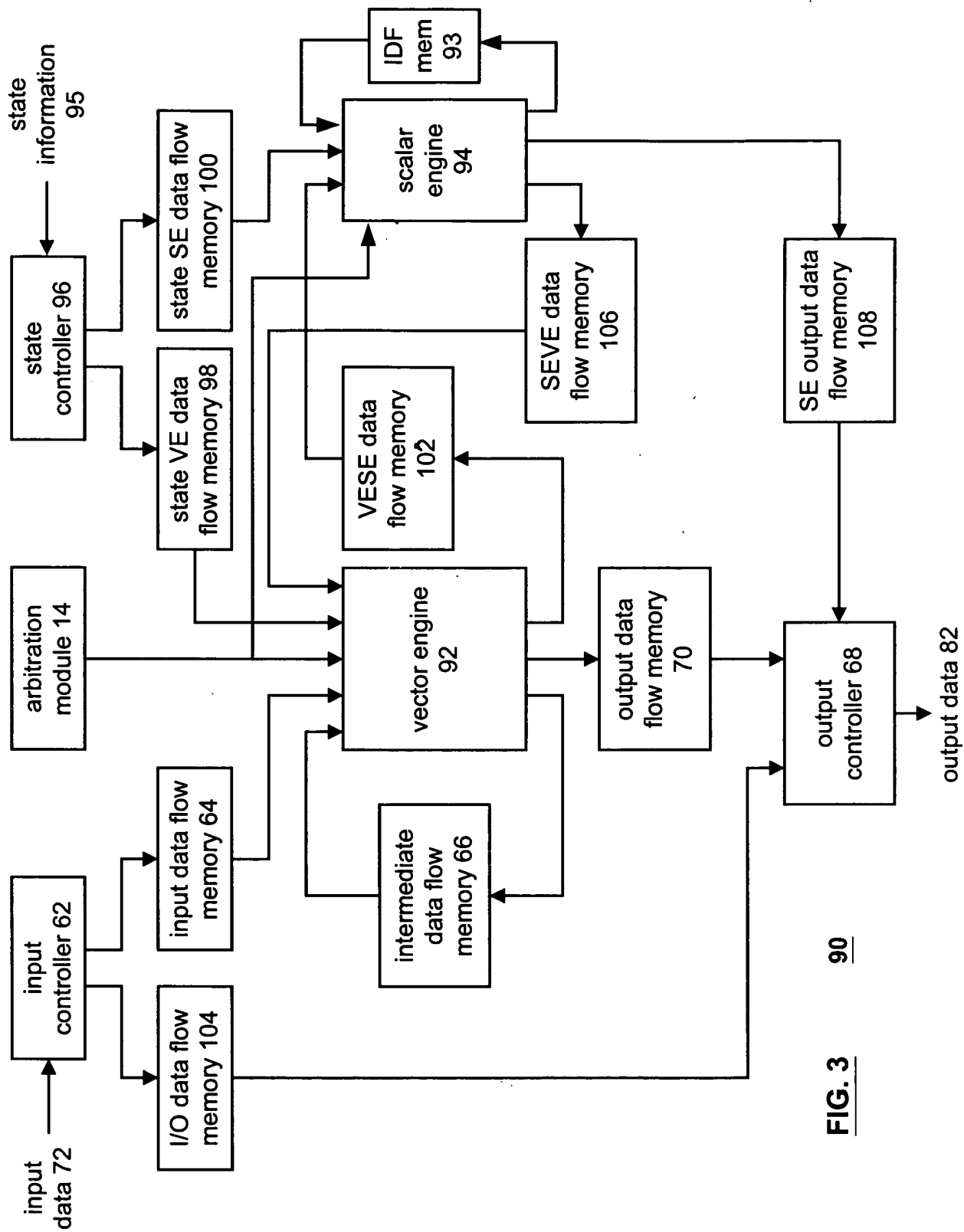


FIG. 3 90

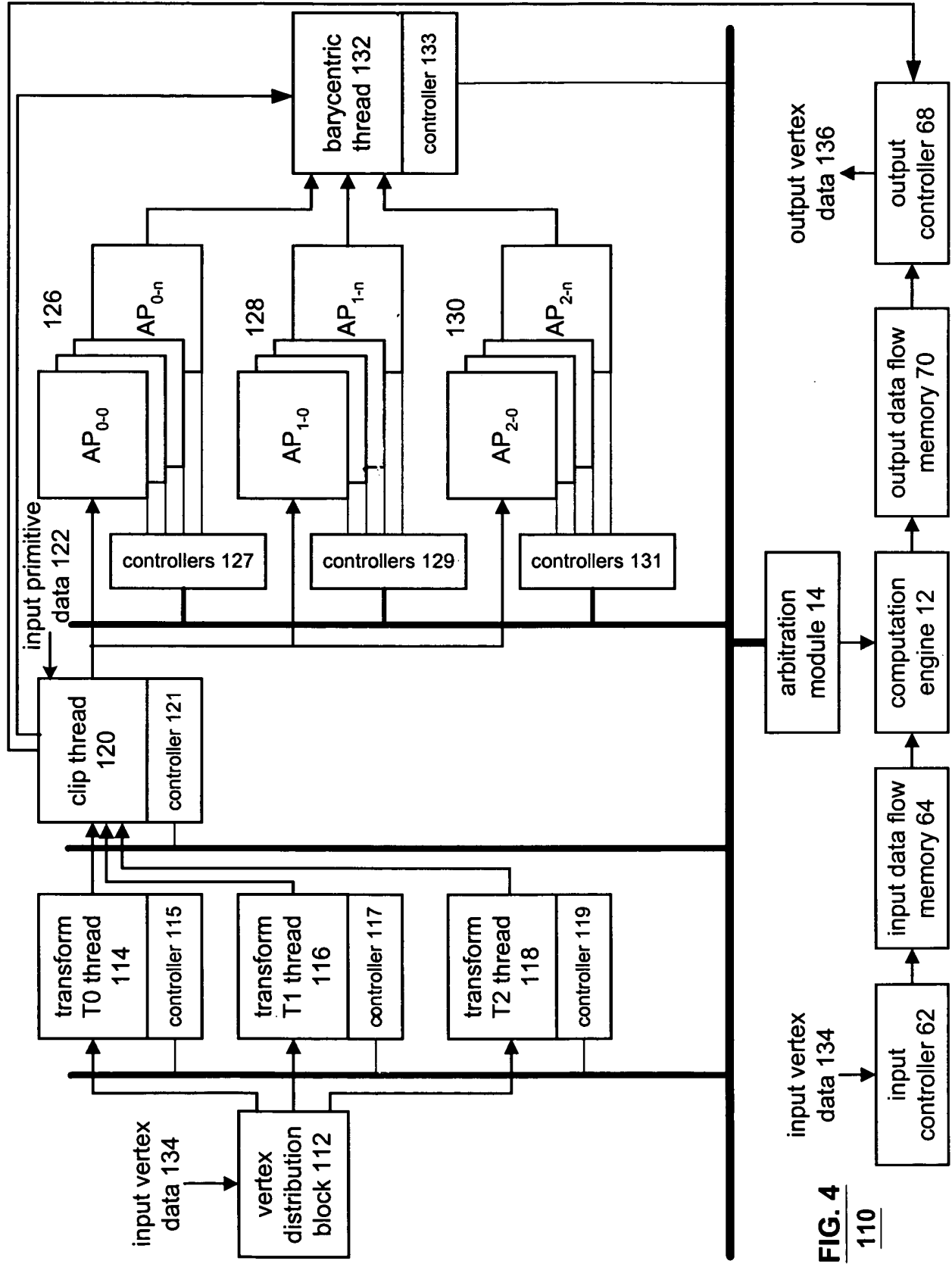


FIG. 4
110

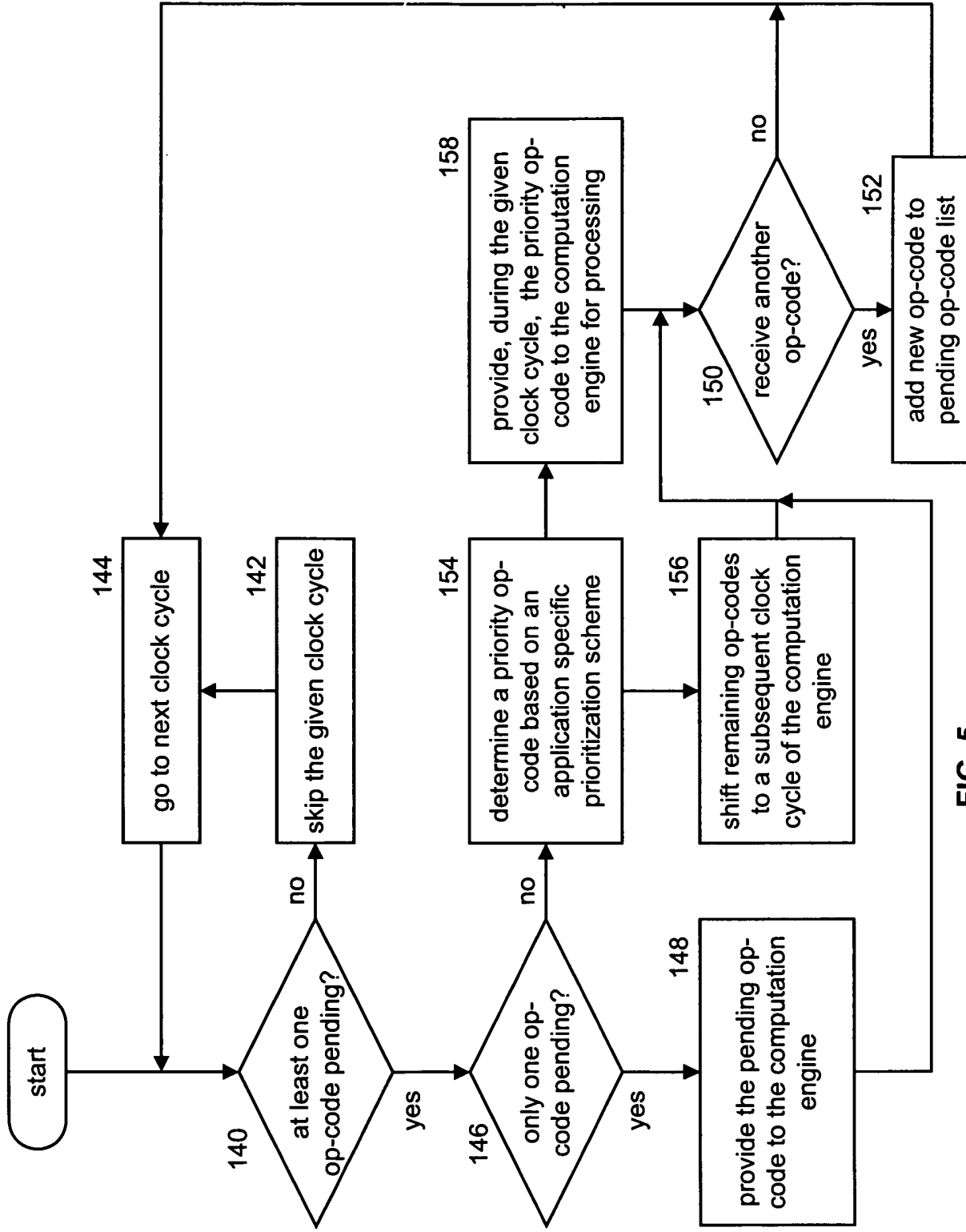


FIG. 5

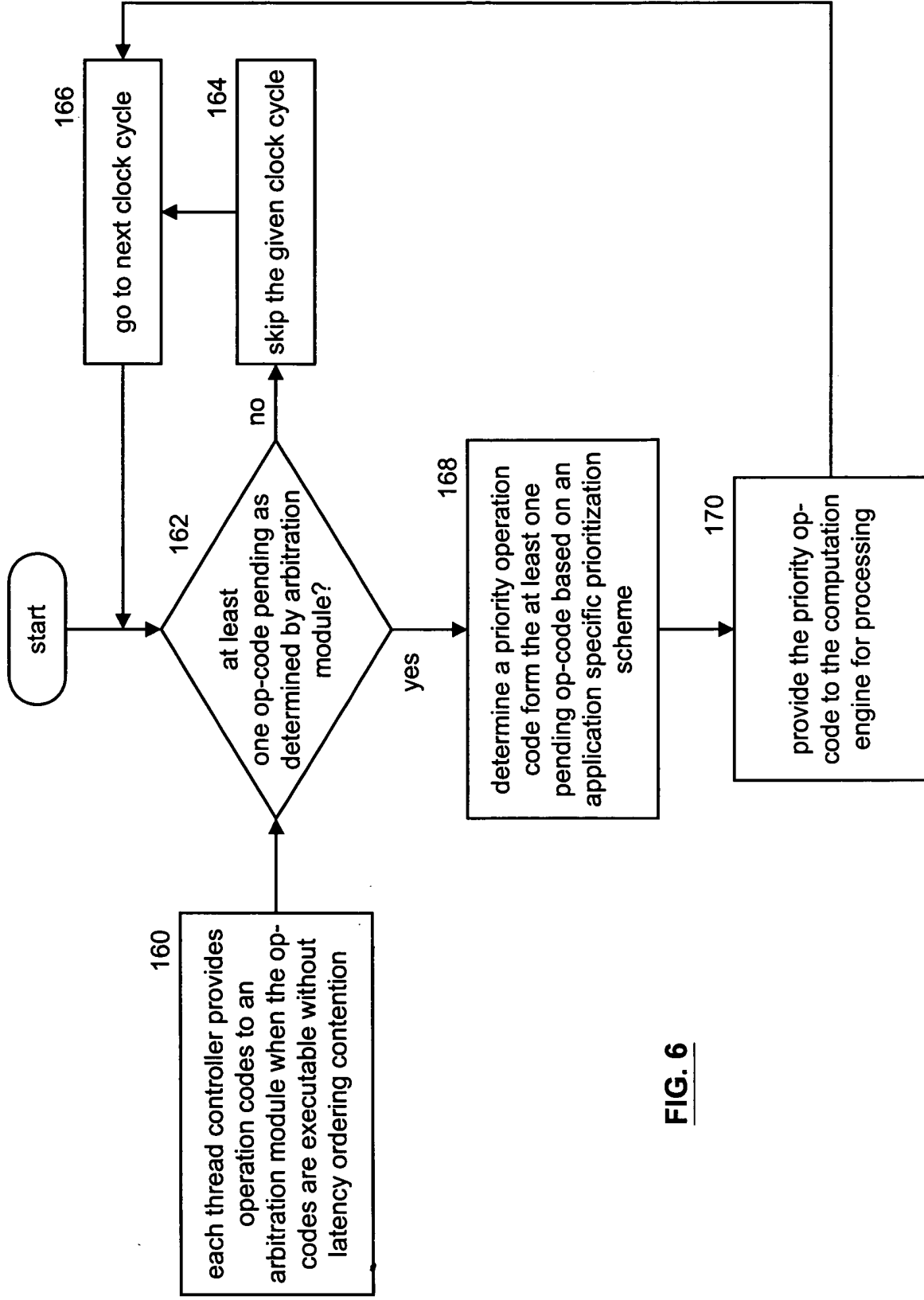
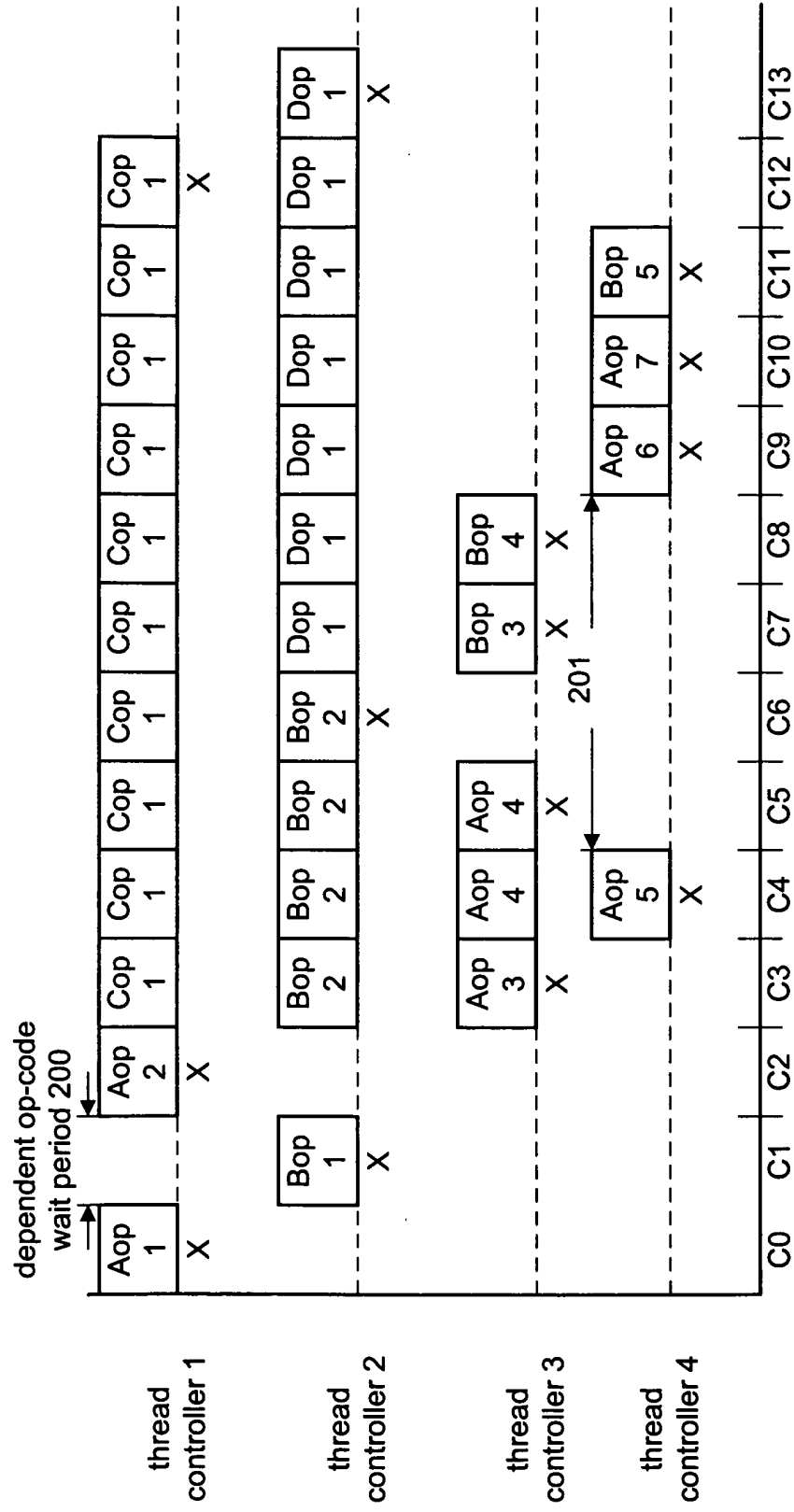


FIG. 6



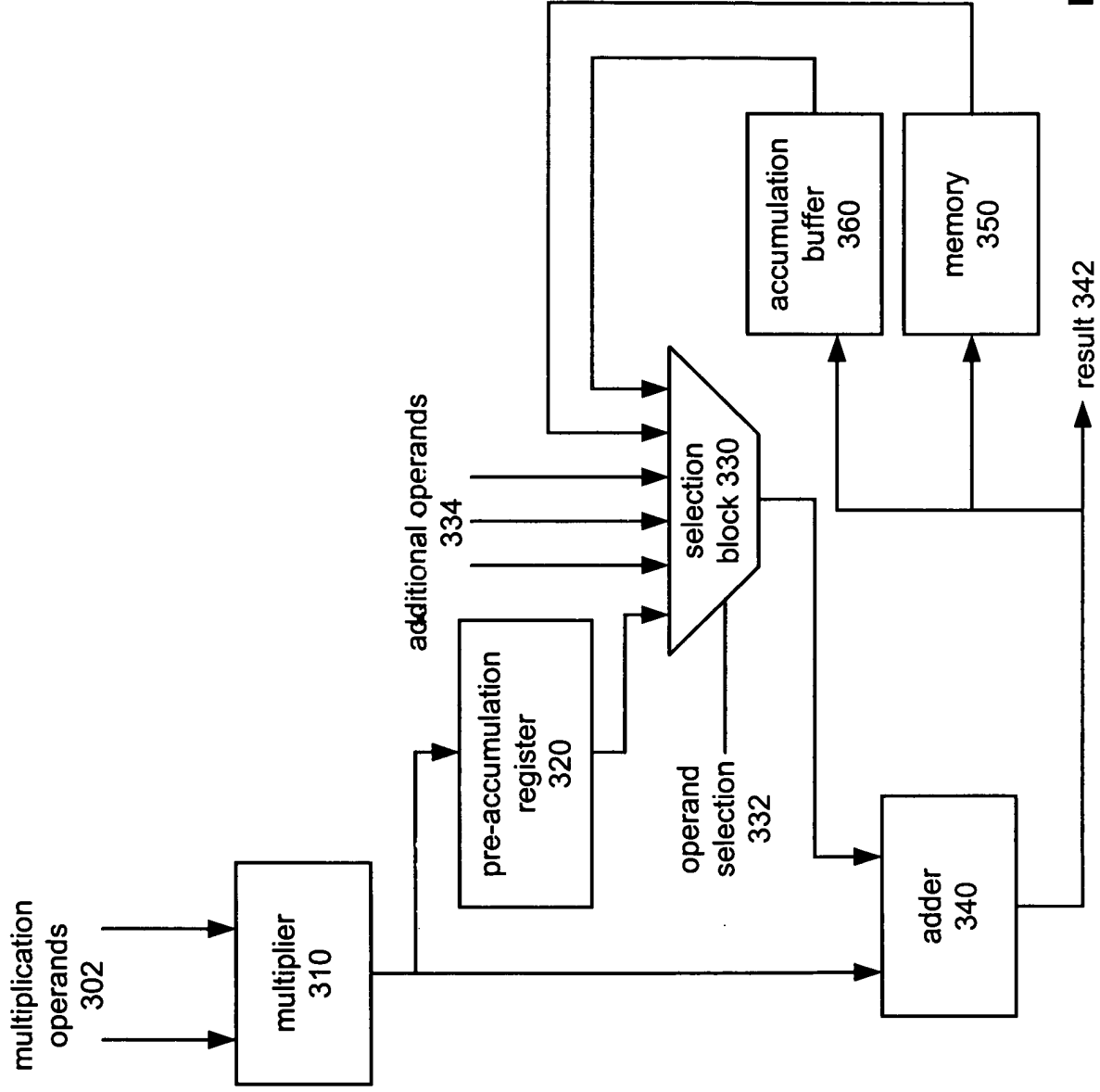
prioritization scheme

thread 4

thread 3

thread 2; thread 1

X indicates selected by arbitration module based on priority scheme



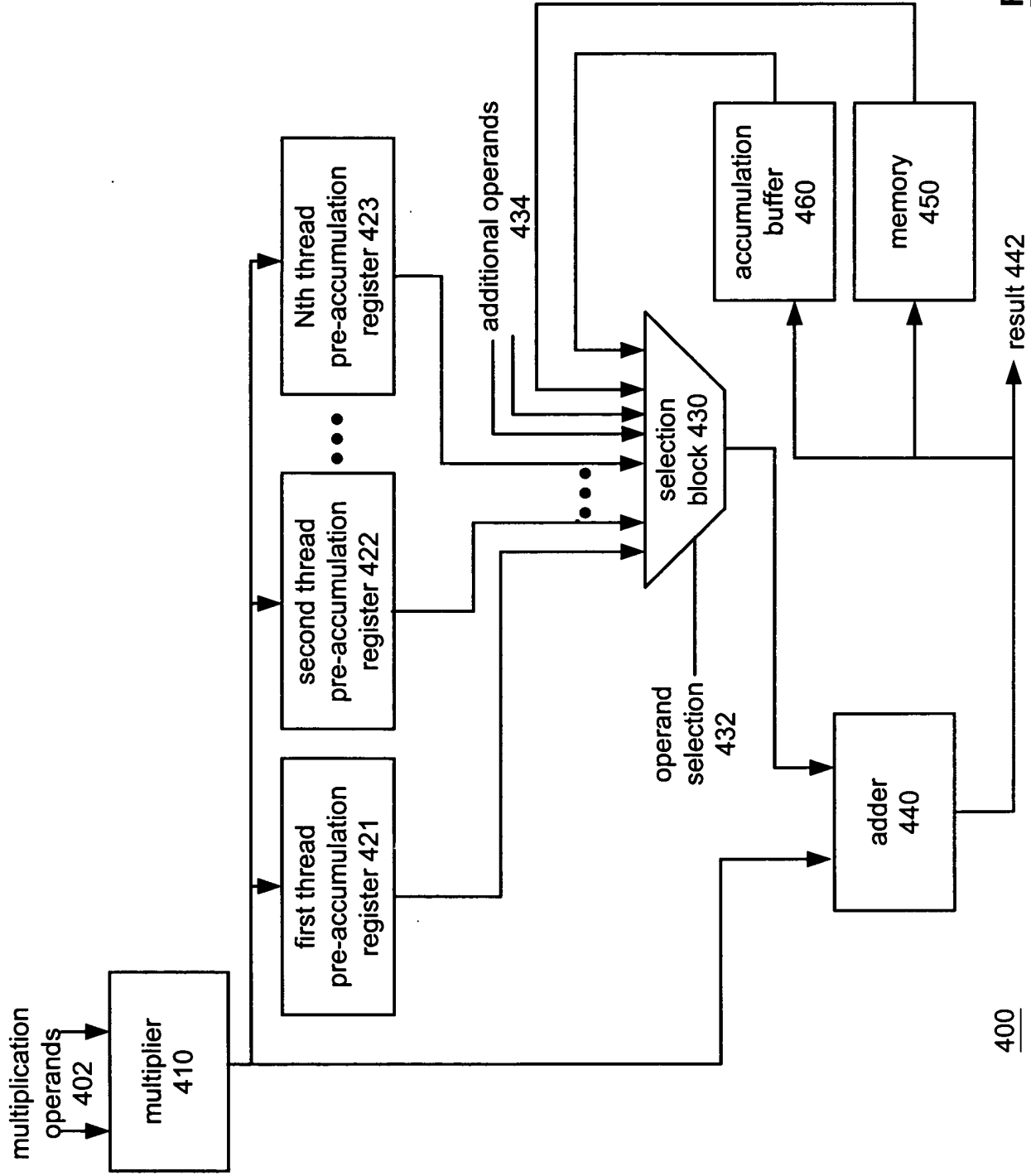
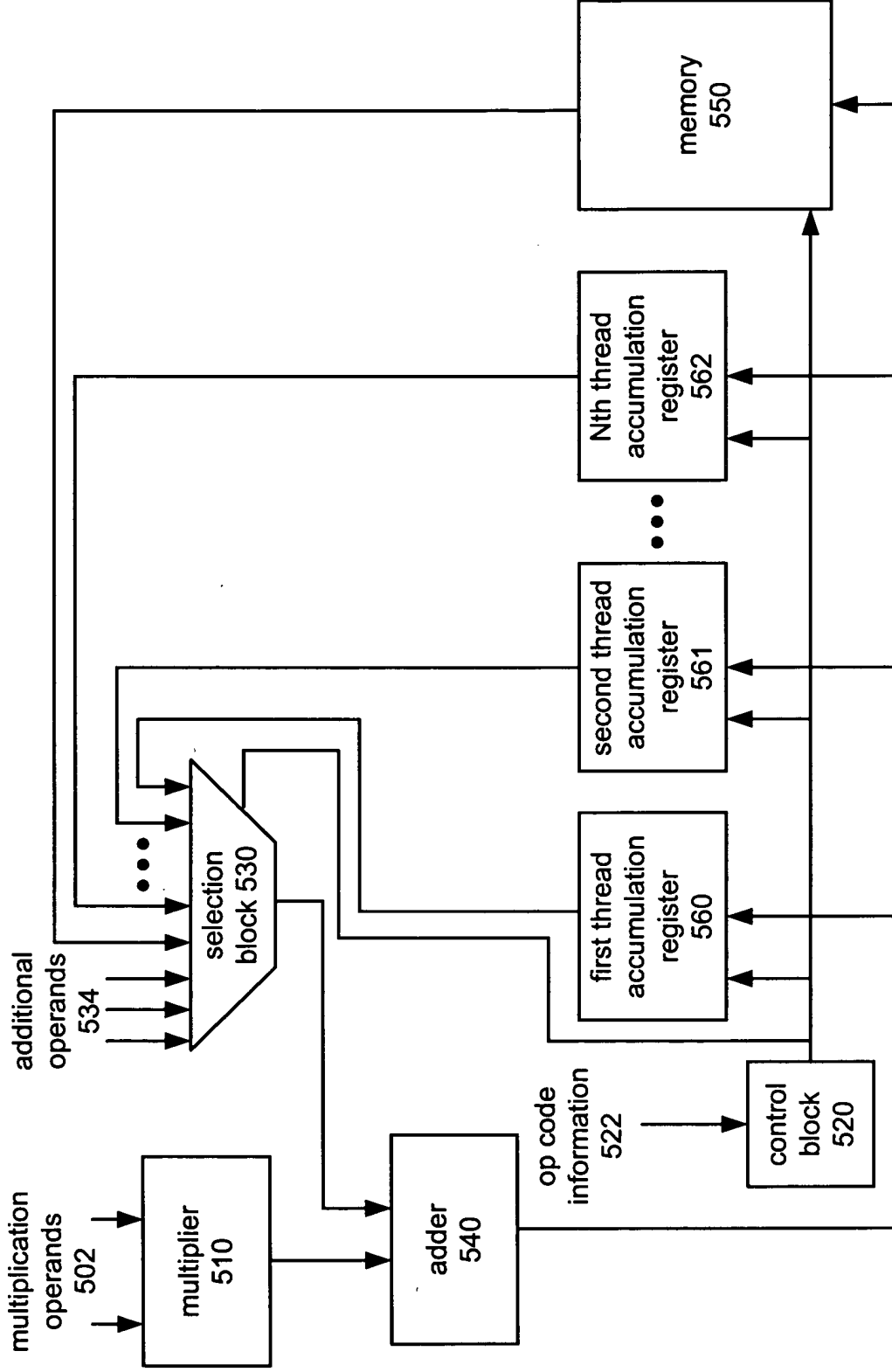
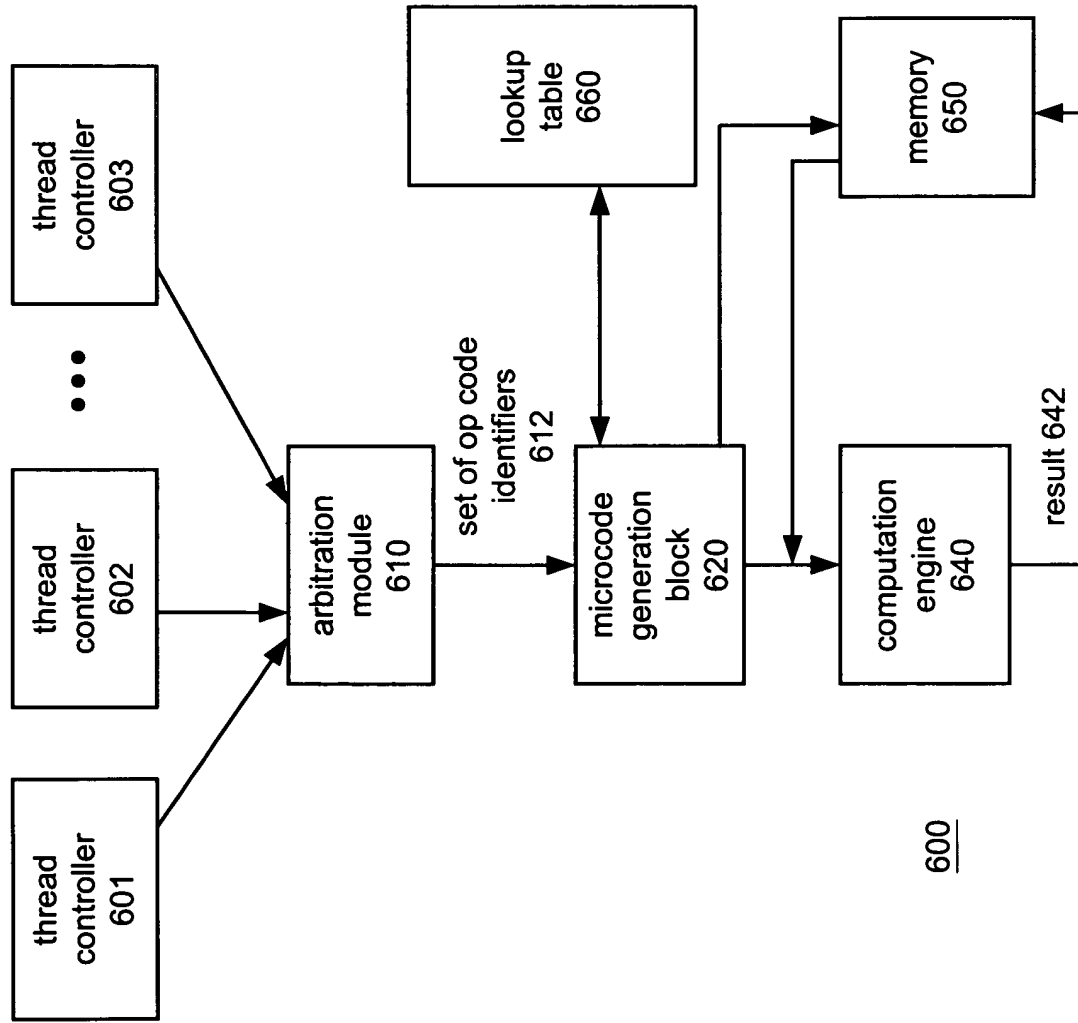


FIG. 9





600

FIG. 11

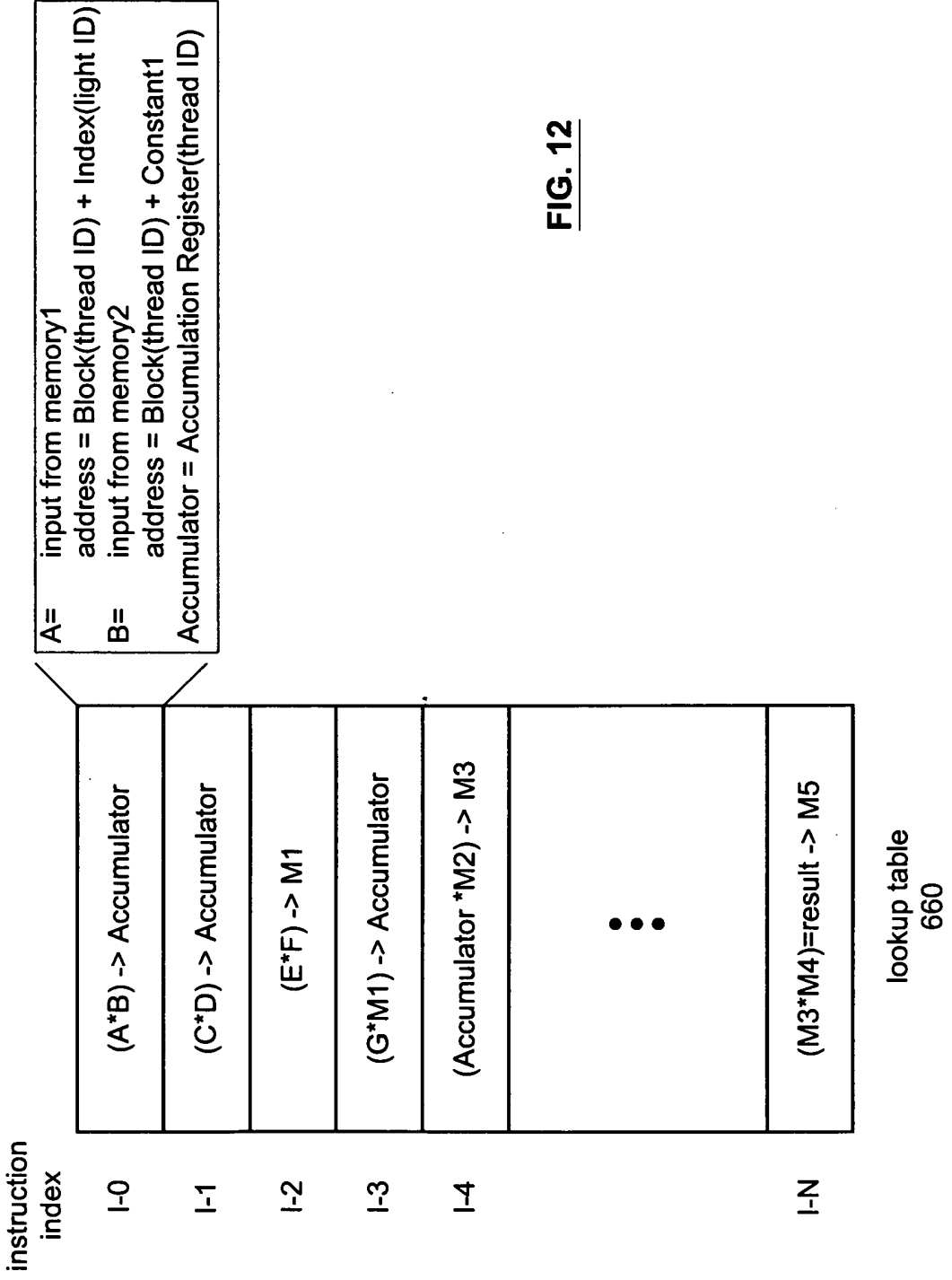


FIG. 12

